

Page01	Power IN
Page02	SOM Interface
Page03	Ethernet-01
Page04	Ethernet-02
Page05	TF & QSPI Flash
Page06	WIFI&BT
Page07	Audio&SPK&SPDIF
Page08	USB3
Page09	USB2
Page10	SPI to UART
Page11	CAN1 & CAN2
Page12	MIPI CSI1&CSI2
Page13	MIPI DSI&HDMI&LV
Page14	RS232 & RS485
Page15	GPIO&RTC&EEPROM&



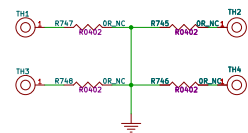
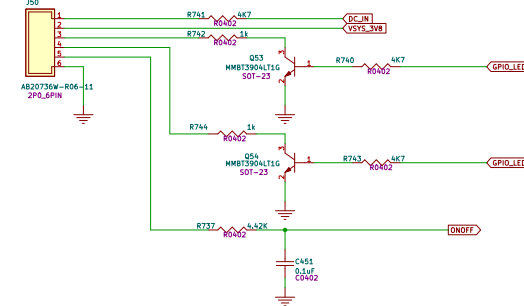
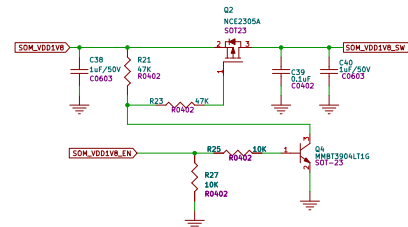
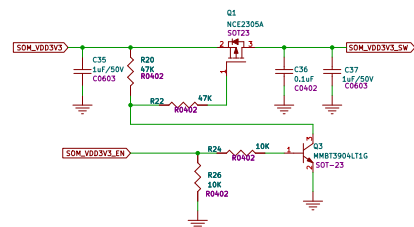
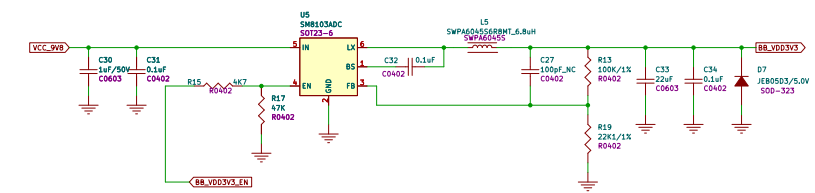
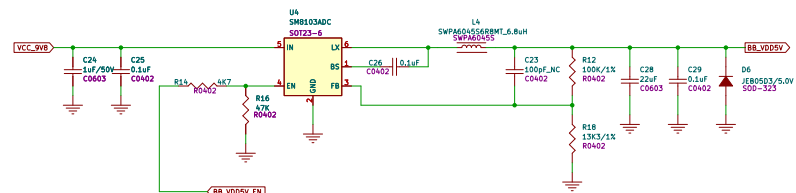
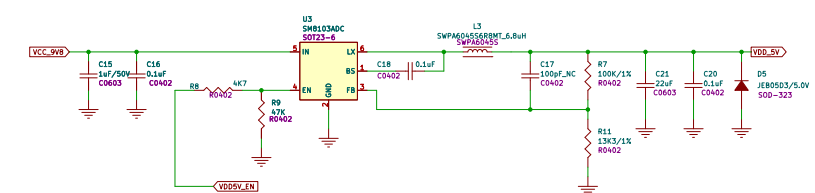
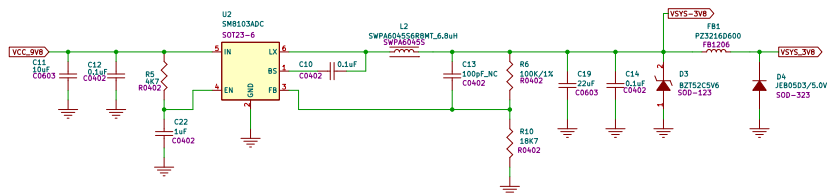
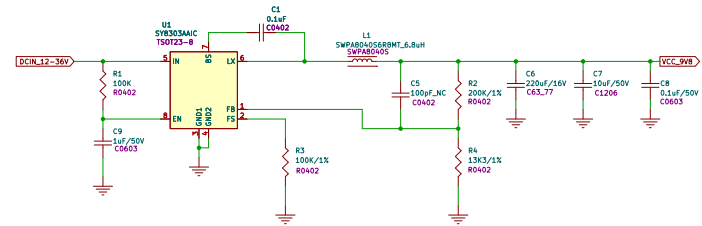
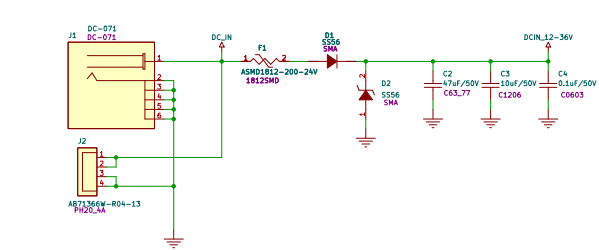
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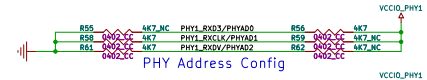
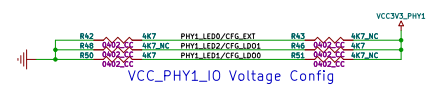
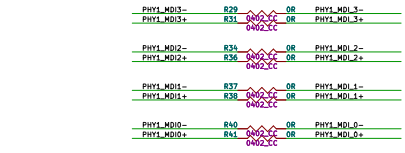
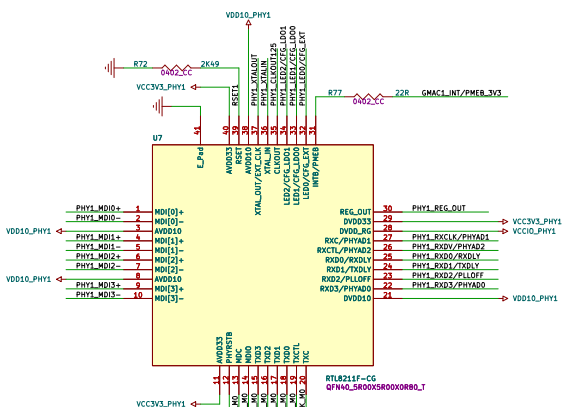
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Title: DEBIX SOM A I/O Board (BMB-08)

Size: A4	Date:	Rev: V1.1
KiCad E.D.A. eeschema (6.0.8)		Id: 1/16

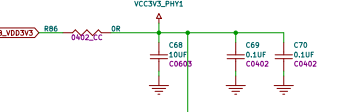
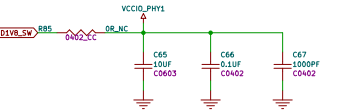
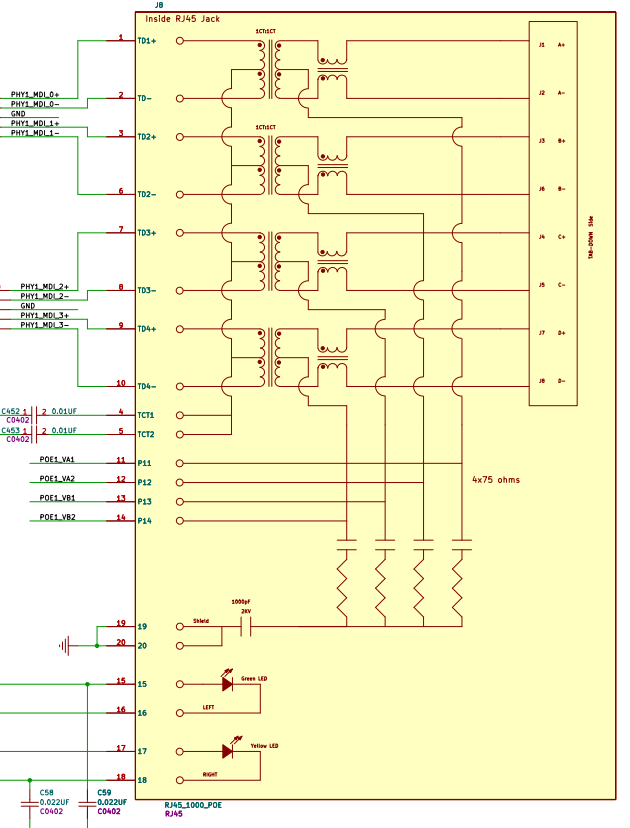
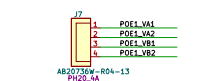
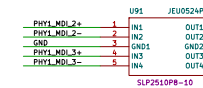
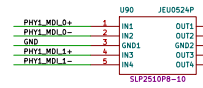
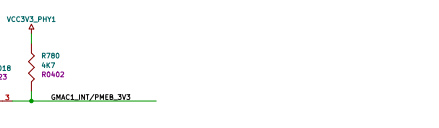
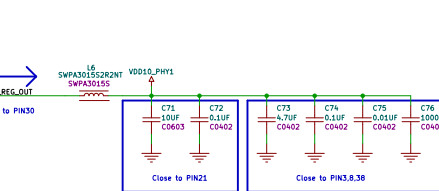
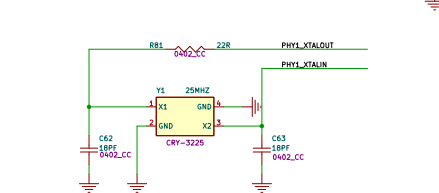
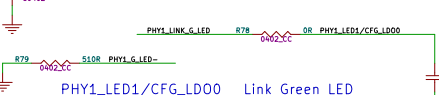
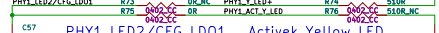


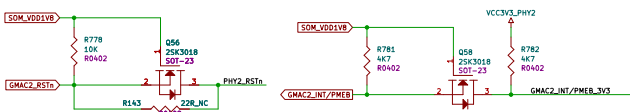
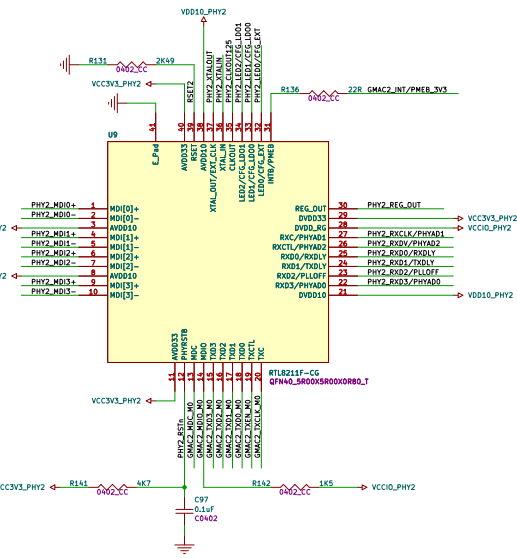
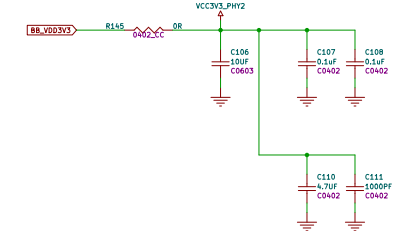
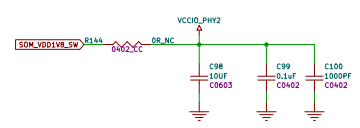
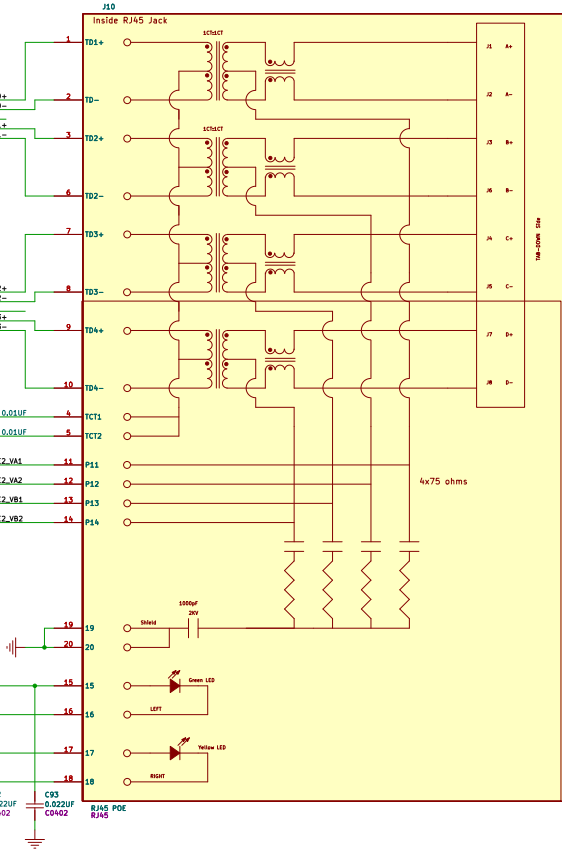
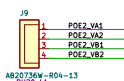
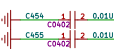
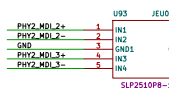
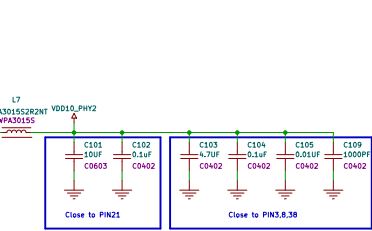
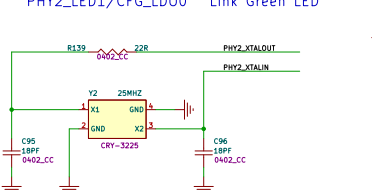
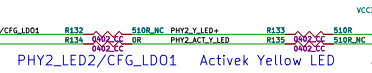
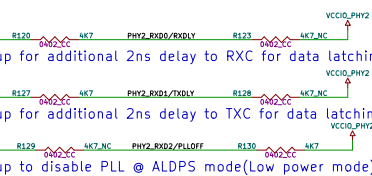
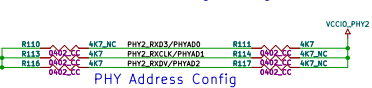
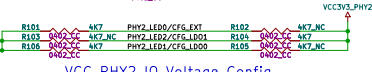
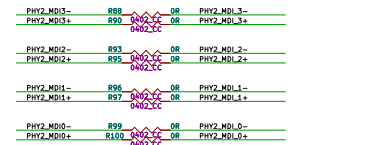


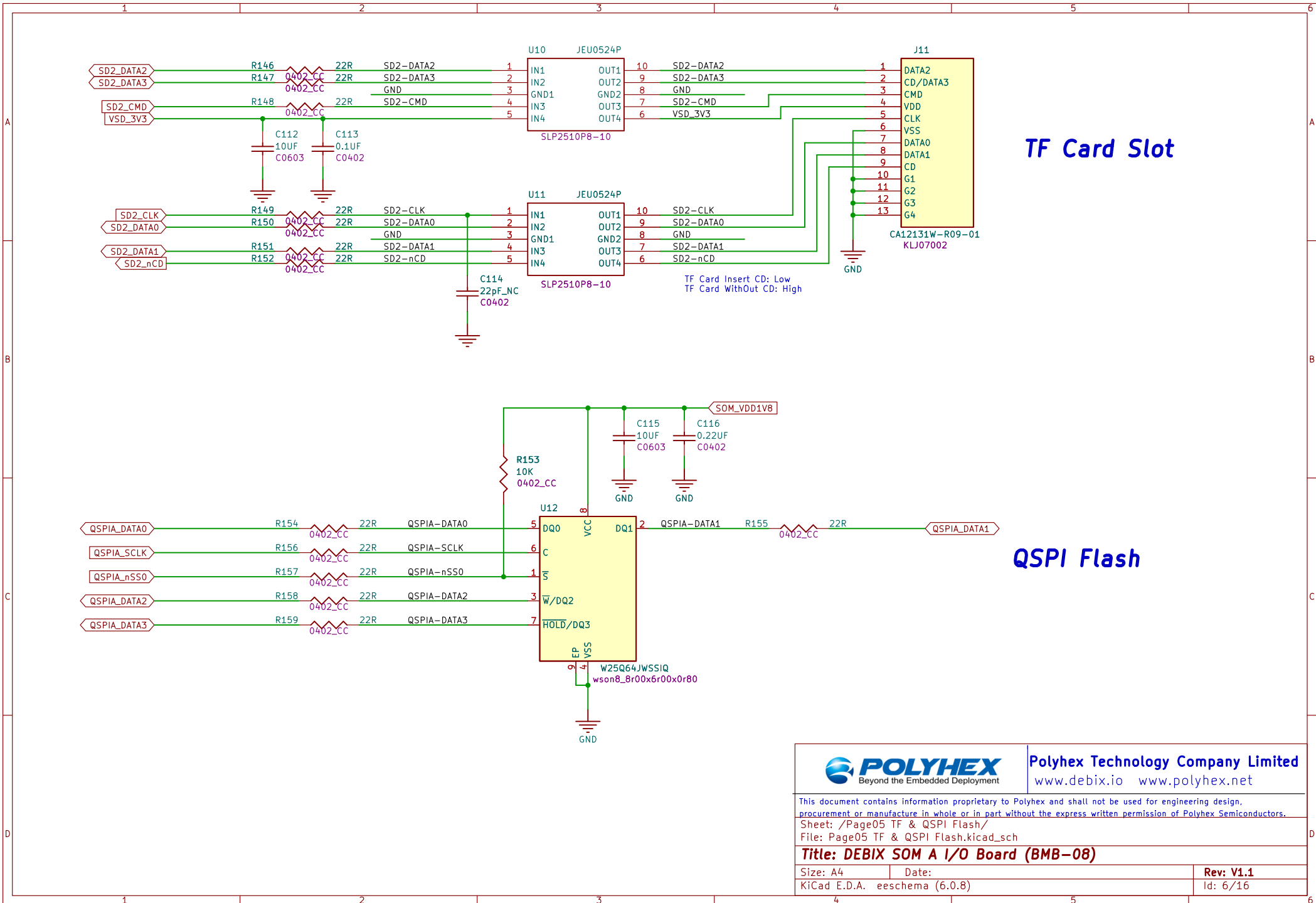
Pull-up for additional 2ns delay to RXC for data latching

Pull-up for additional 2ns delay to TXC for data latching

Pull-up to disable PLL @ ALDPS mode(Low power mode)





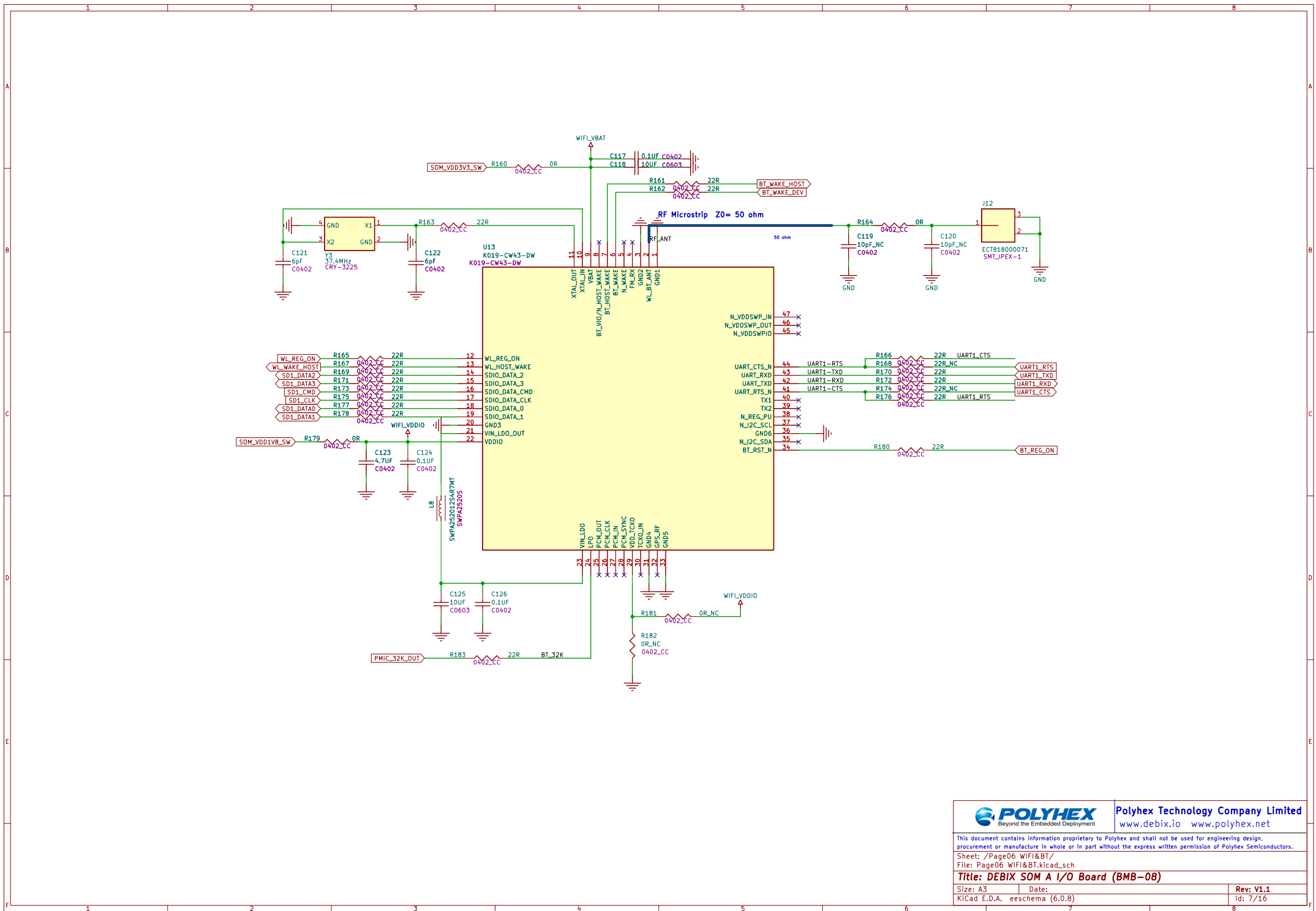


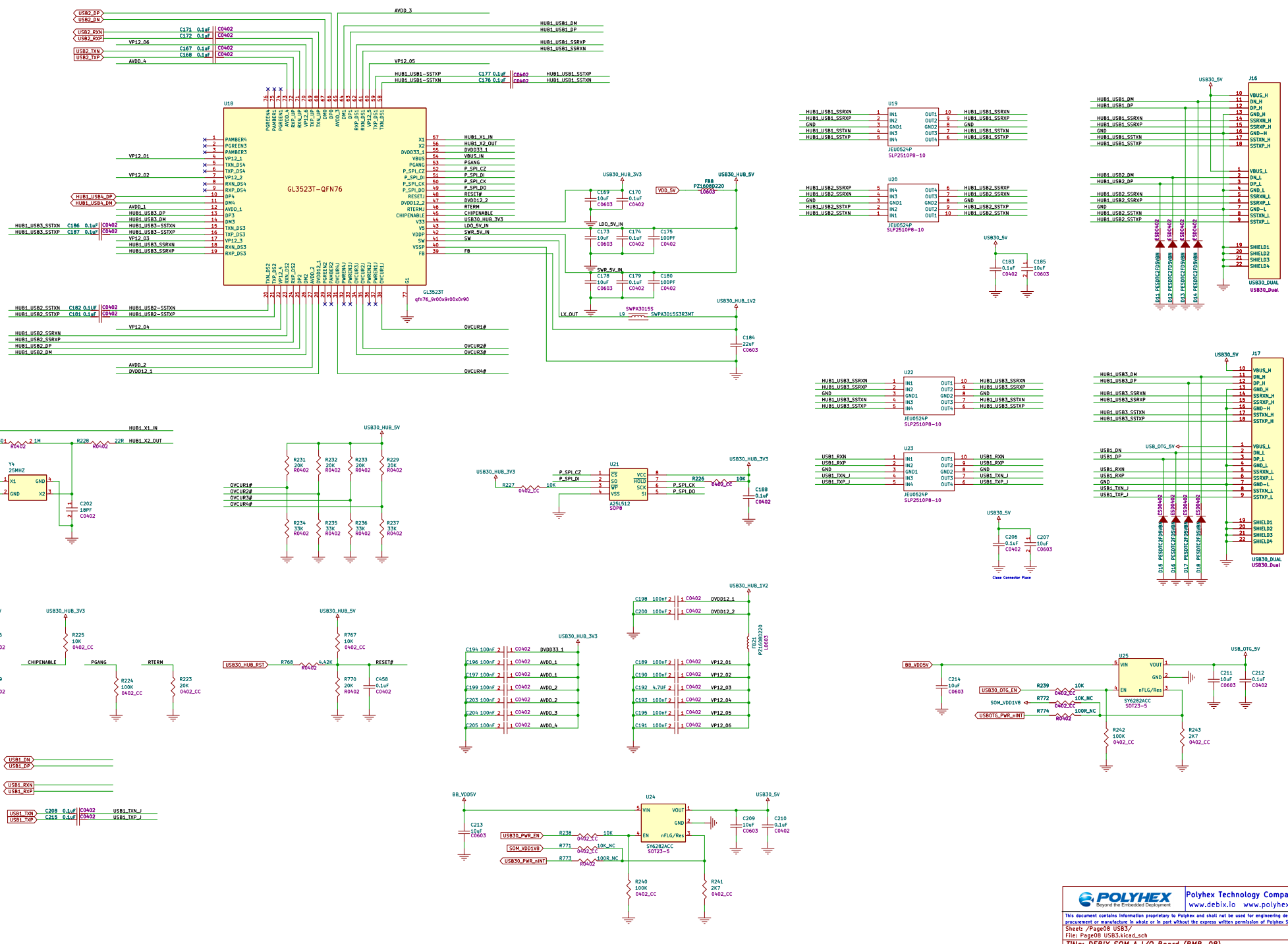
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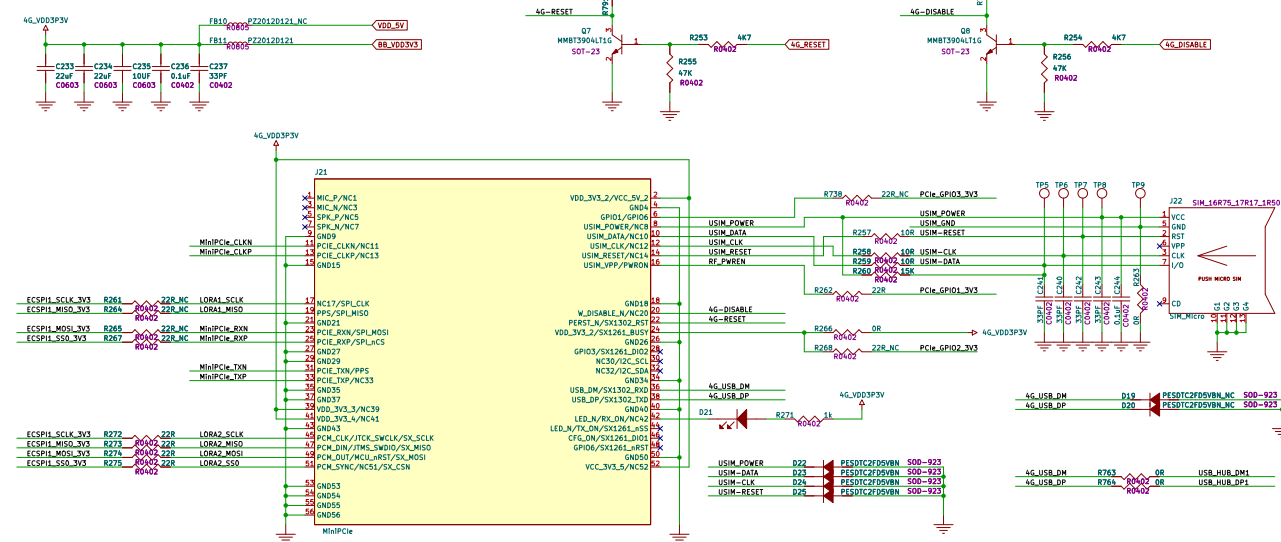
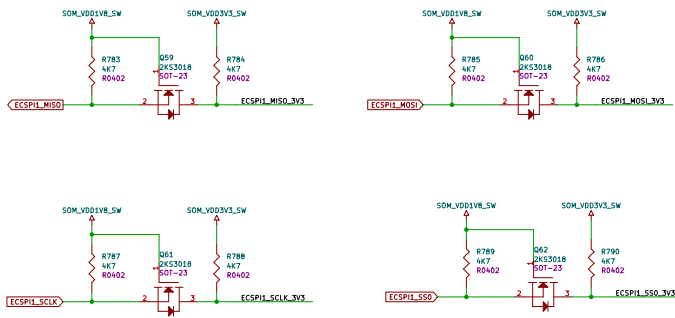
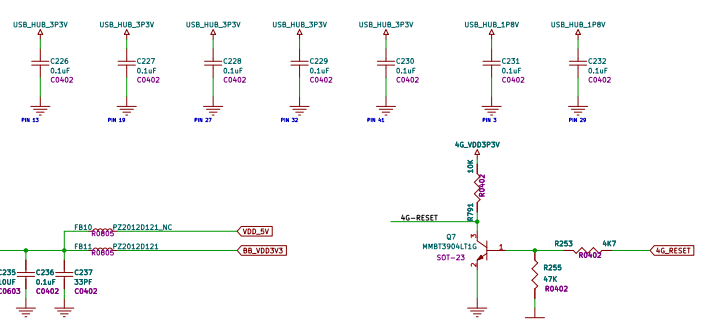
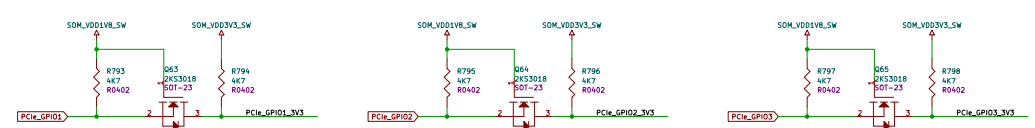
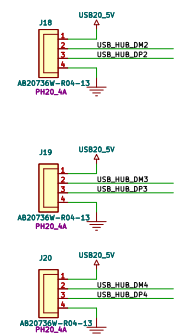
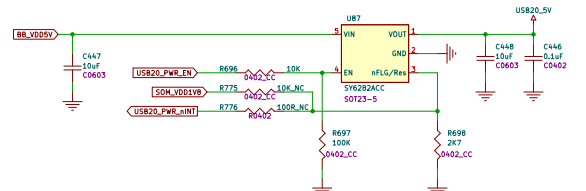
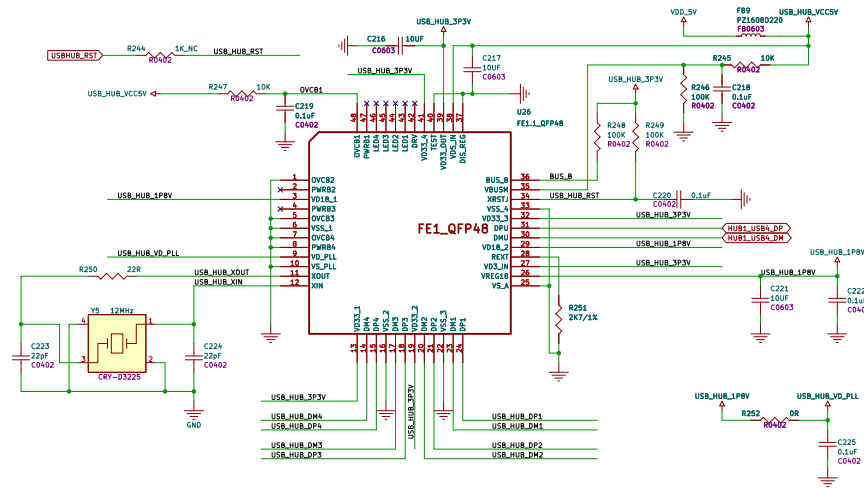
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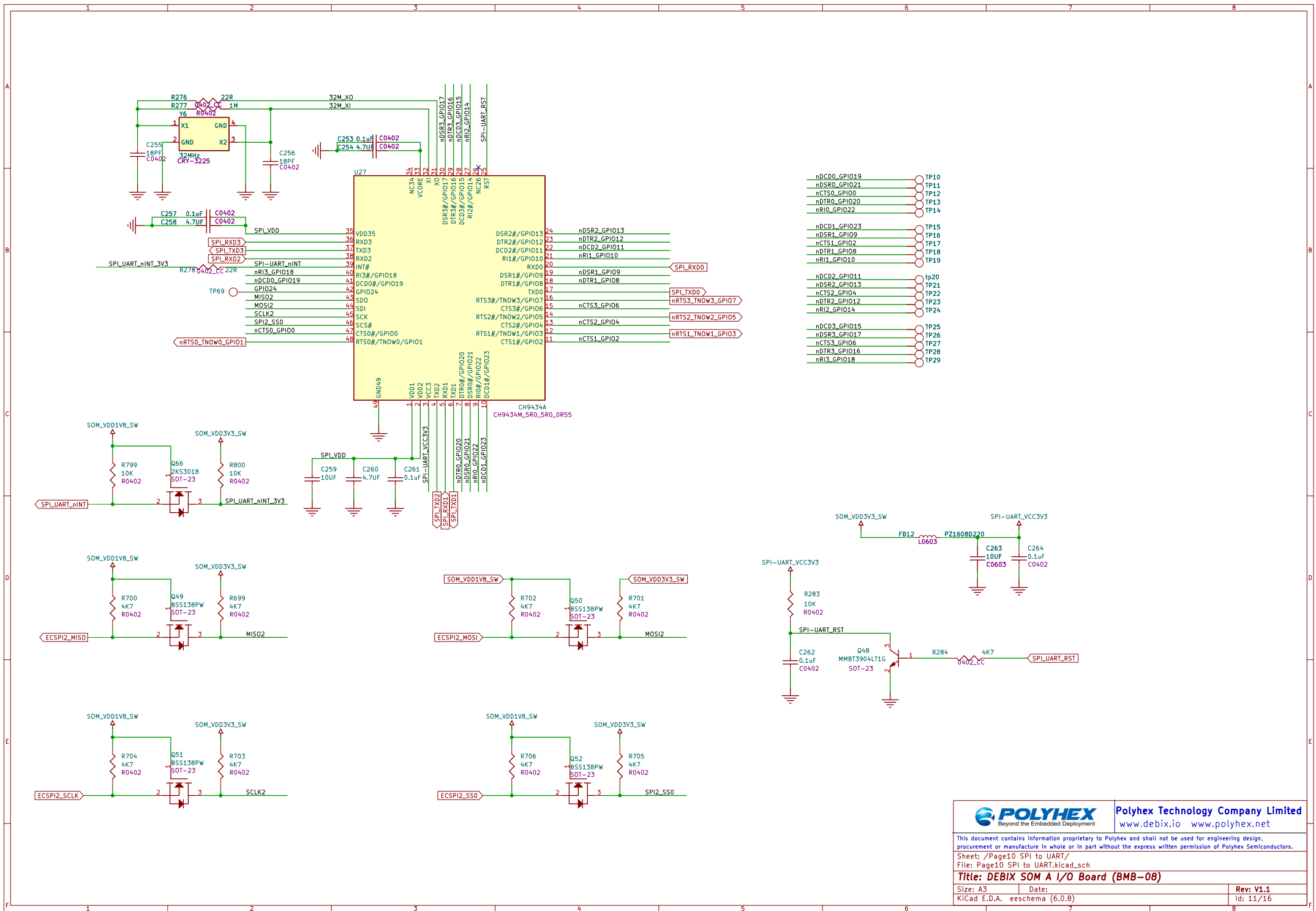
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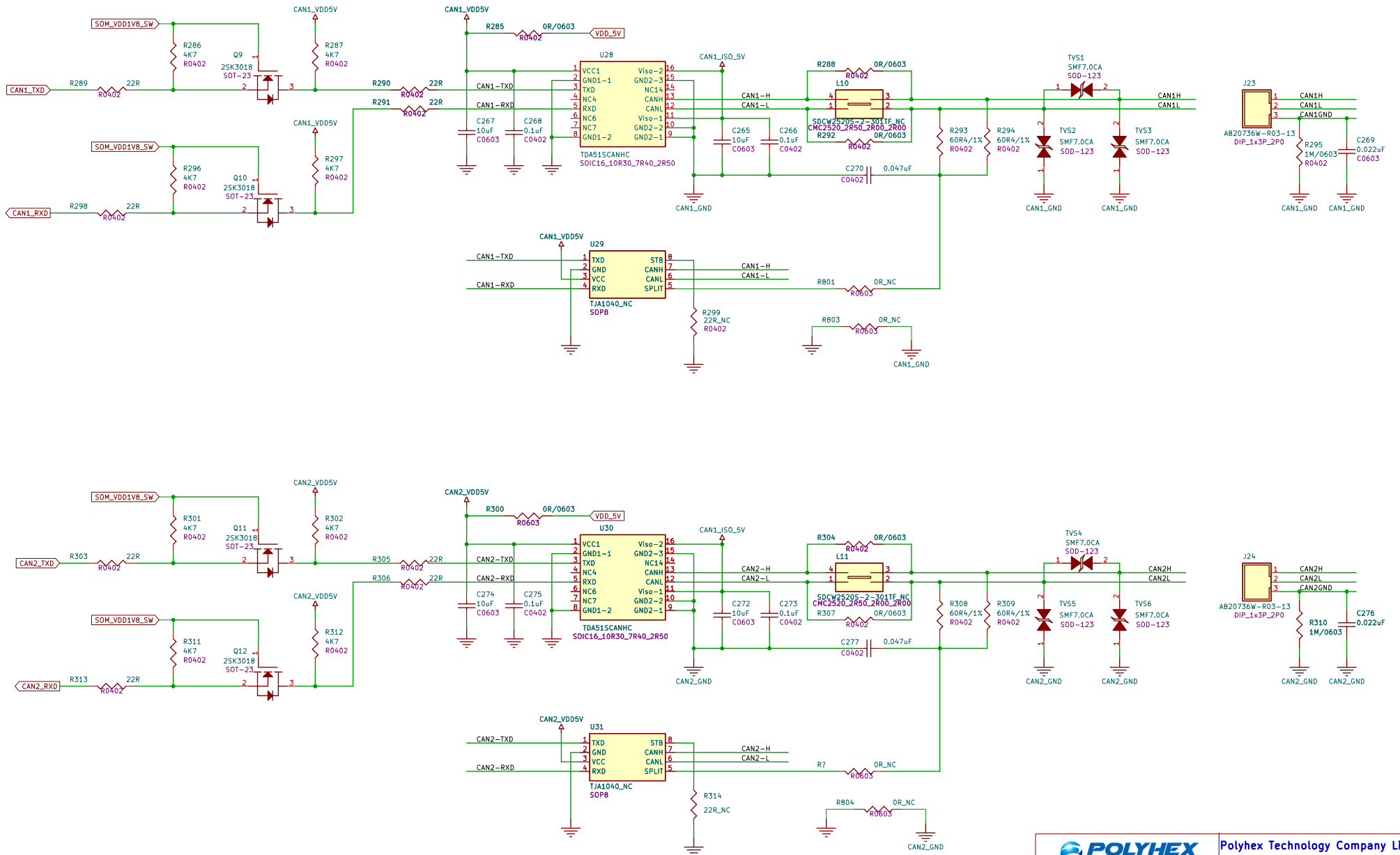
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